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June 2002 Revised March 2004

NC7SV86 TinyLogic® ULP-A 2-Input Exclusive-OR Gate

General Description

FAIRCHILD

SEMICONDUCTOR

The NC7SV86 is a single 2-Input Exclusive-OR Gate from Fairchild's Ultra Low Power-A (ULP-A) Series of TinyLogic®. ULP-A is ideal for applications that require extreme high speed, high drive and low power. This product is designed for a wide low voltage operating range (0.9V to 3.6V V_{CC}) and applications that require more drive and speed than the TinyLogic ULP series, but still offer best in class low power operation.

The NC7SV86 is uniquely designed for optimized power and speed, and is fabricated with an advanced CMOS technology to achieve high-speed operation while maintaining low CMOS power dissipation.

Features

- \blacksquare 0.9V to 3.6V V_{CC} supply operation
- 3.6V overvoltage tolerant I/O's at V_{CC} from 0.9V to 3.6V
- Extremely High Speed t_{PD}
- 1.0 ns typ for 2.7V to 3.6V V_{CC}
- 1.2 ns typ for 2.3V to 2.7V V_{CC}
- 1.9 ns typ for 1.65V to 1.95V V_{CC}
- 3.2 ns typ for 1.4V to 1.6V V_{CC}
- 6.0 ns typ for 1.1V to 1.3V $V_{\mbox{CC}}$
- 15.0 ns typ for 0.9V V_{CC}
- Power-Off high impedance inputs and outputs
- High Static Drive (I_{OH}/I_{OL})
- ± 24 mA ~ @ 3.00V V_{CC}
- ± 18 mA ~ @ 2.30V V_{CC}
- ± 6 mA ~ @ 1.65V V_{CC}
- ± 4 mA ~ @ 1.4V V_{CC}
- $\pm 2~\text{mA}$ $@~1.1V~V_{CC}$
- $\pm 0.1 \text{ mA}$ @ 0.9V V_{CC}
- Uses patented Quiet Series[™] noise/EMI reduction circuitry

TinyLogic ULP and ULP-A with up to 50% less power consumption can

Assumes ideal 3.6V Lithium Ion battery with current rating of 900mAH and

derated 90% and device frequency at 10MHz, with $C_L = 15 \text{ pF}$ load

- Ultra small MicroPak[™] leadfree package
- Ultra low dynamic power

extend your battery life significantly.

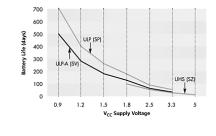
Battery Life = (V_{battery} *I_{battery} *.9)/(P_{device})/24hrs/day

Where, $P_{device} = (I_{CC} * V_{CC}) + (C_{PD} + C_L) * V_{CC}^2 * f$

Ordering Code:

Order Number	Package Number	Product Code Top Mark	Package Description	Supplied As	
NC7SV86P5X	MAA05A	V86	5-Lead SC70, EIAJ SC-88a, 1.25mm Wide	3k Units on Tape and Reel	
NC7SV86L6X	MAC06A	H5	6-Lead MicroPak, 1.0mm Wide	5k Units on Tape and Reel	

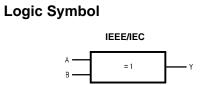
Battery Life vs. V_{CC} Supply Voltage



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NC7SV86



Pin Descriptions

Pin Names	Description
А, В	Input
Y	Output
NC	No Connect

Function Table

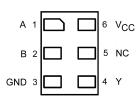
$\mathbf{Y} = \mathbf{A} \oplus \mathbf{B}$							
Inp	out	Output					
Α	В	Y					
L	L	L					
L	н	Н					
Н	L	н					
Н	Н	L					

H = HIGH Logic Level L = LOW Logic Level

Pin Assignments for SC70

Connection Diagrams

Pad Assignment for MicroPak



(Top Thru View)

Absolute	Maximum	Ratings(Note 1)
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Recommended Operating

NC7SV86

	•	Conditions (Note 3)	•
Supply Voltage (V _{CC})	-0.5V to +4.6V		
DC Input Voltage (V _{IN})	-0.5V to +4.6V	Supply Voltage	0.9V to 3.6V
DC Output Voltage (V _{OUT})		Input Voltage (V _{IN})	0V to 3.6V
HIGH or LOW State (Note 2)	–0.5V to V_CC +0.5V	Output Voltage (V _{OUT})	
$V_{CC} = 0V$	-0.5V to +4.6V	$V_{CC} = 0.0V$	0V to 3.6V
DC Input Diode Current (I _{IK}) $V_{IN} < 0V$	±50 mA	HIGH or LOW State	0V to V_{CC}
DC Output Diode Current (I _{OK})		Output Current in I _{OH} /I _{OL}	
V _{OUT} < 0V	–50 mA	$V_{CC} = 3.0V$ to 3.6V	±24 mA
V _{OUT} > V _{CC}	+50 mA	$V_{CC} = 2.3V$ to 2.7V	±18 mA
DC Output Source/Sink Current (I_{OH}/I_{OL})	± 50 mA	V _{CC} = 1.65V to 1.95V	±6 mA
DC V_{CC} or Ground Current per		$V_{CC} = 1.4V$ to 1.6V	±4 mA
Supply Pin (I _{CC} or Ground)	± 50 mA	V _{CC} = 1.1V to 1.3V	±2 mA
Storage Temperature Range (T _{STG})	$-65^{\circ}C$ to $+150^{\circ}C$	$V_{CC} = 0.9V$	±0.1 mA
		Free Air Operating Temperature (T _A)	$-40^{\circ}C$ to $+85^{\circ}C$

Minimum Input Edge Rate ($\Delta t/\Delta V$)

 V_{IN} = 0.8V to 2.0V, V_{CC} = 3.0V 10 ns/V

Note 1: Absolute Maximum Ratings: are those values beyond which the safety of the device cannot be guaranteed. The device should not be oper-ated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Note 2: I_{O} Absolute Maximum Rating must be observed.

Note 3: Unused inputs must be held HIGH or LOW. They may not float.

Symbol	Parameter	V _{cc}	$T_A = +25^{\circ}C$		$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$		Units	Conditions
•	i ulumotor	(V)	Min	Max	Min	Max	Units	conditions
VIH	HIGH Level	0.90	$0.65 \times V_{CC}$		0.65 x V _{CC}			
	Input Voltage	$1.10 \leq V_{CC} \leq 1.30$	$0.65 \times V_{CC}$		$0.65 \times V_{CC}$			
		$1.40 \leq V_{CC} \leq 1.60$	$0.65 \times V_{CC}$		$0.65 \times V_{CC}$		v	
		$1.65 \leq V_{CC} \leq 1.95$	$0.65 \times V_{CC}$		$0.65 \times V_{CC}$		v	
		$2.30 \leq V_{CC} < 2.70$	1.6		1.6			
		$2.70 \leq V_{CC} \leq 3.60$	2.0		2.0			
VIL	LOW Level	0.90		0.35 x V _{CC}		0.35 x V _{CC}		
	Input Voltage	$1.10 \leq V_{CC} \leq 1.30$		$0.35 ext{ x V}_{CC}$		$0.35 \times V_{CC}$		
		$1.40 \leq V_{CC} \leq 1.60$		$0.35 ext{ x V}_{CC}$		$0.35 \times V_{CC}$	v	
		$1.65 \leq V_{CC} \leq 1.95$		$0.35 ext{ x V}_{CC}$		$0.35 \times V_{CC}$	v	
		$2.30 \leq V_{CC} < 2.70$		0.7		0.7		
		$2.70 \leq V_{CC} \leq 3.60$		0.8		0.8		
V _{OH}	HIGH Level	0.90	V _{CC} - 0.1		V _{CC} - 0.1			
	Output Voltage	$1.10 \leq V_{CC} \leq 1.30$	$V_{CC} - 0.1$		$V_{CC} - 0.1$			
		$1.40 \leq V_{CC} \leq 1.60$	$V_{CC} - 0.2$		$V_{CC} - 0.2$			I _{OH} = -100 μA
		$1.65 \leq V_{CC} \leq 1.95$	$V_{CC} - 0.2$		$V_{CC} - 0.2$			IOH = -100 μA
		$2.30 \leq V_{CC} < 2.70$	$V_{CC} - 0.2$		$V_{CC} - 0.2$			
		$2.70 \leq V_{CC} \leq 3.60$	$V_{CC} - 0.2$		$V_{CC} - 0.2$			
		$1.10 \leq V_{CC} \leq 1.30$	$0.75 \times V_{CC}$		0.75 x V _{CC}			$I_{OH} = -2 \text{ mA}$
		$1.40 \leq V_{CC} \leq 1.60$	$0.75 \times V_{CC}$		0.75 x V _{CC}		V	$I_{OH} = -4 \text{ mA}$
		$1.65 \leq V_{CC} \leq 1.95$	1.25		1.25			I _{OH} = -6 mA
		$2.30 \le V_{CC} < 2.70$	2.0		2.0			IOH0 IIIY
		$2.30 \le V_{CC} < 2.70$	1.8		1.8			I _{OH} = -12 mA
		$2.70 \leq V_{CC} \leq 3.60$	2.2		2.2			OH 12 MA
		$2.30 \le V_{CC} < 2.70$	1.7		1.7			I _{OH} = -18 mA
		$2.70 \leq V_{CC} \leq 3.60$	2.4		2.4			10H 10 IIIA
		$2.70 \le V_{CC} \le 3.60$	2.2		2.2			I _{OH} = -24 mA

DC Electrical Characteristics

SV86

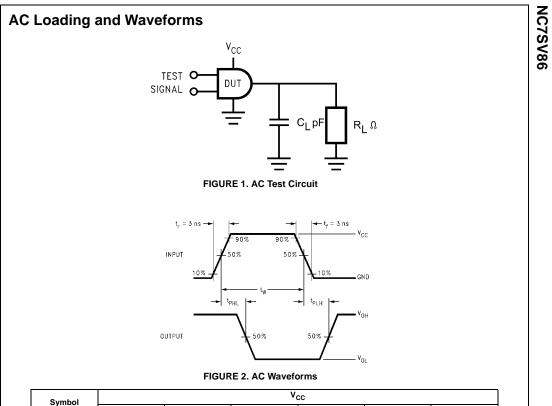
DC Electrical Characteristics (Continued)

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Symbol	Parameter	V _{cc}	$T_A = +25^{\circ}C$		$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$		Units	Conditions
Gymbol		(V)	Min	Max	Min	Max	Units	Conditions
V _{OL}	LOW Level	0.90		0.1		0.1		
	Output Voltage	$1.10 \leq V_{CC} \leq 1.30$		0.1		0.1		
		$1.40 \leq V_{CC} \leq 1.60$		0.2		0.2		I _{OL} = 100 μA
		$1.65 \leq V_{CC} \leq 1.95$		0.2		0.2		$I_{OL} = 100 \mu A$
		$2.30 \leq V_{CC} < 2.70$		0.2		0.2		
		$2.70 \leq V_{CC} \leq 3.60$		0.2		0.2		
		$1.10 \leq V_{CC} \leq 1.30$		0.25 x V _{CC}		0.25 x V _{CC}	v	$I_{OL} = 2 \text{ mA}$
		$1.40 \leq V_{CC} \leq 1.60$		0.25 x V _{CC}		0.25 x V _{CC}	v	$I_{OL} = 4 \text{ mA}$
		$1.65 \leq V_{CC} \leq 1.95$		0.3		0.3		$I_{OL} = 6 \text{ mA}$
		$2.30 \le V_{CC} < 2.70$		0.4		0.4	•	I _{OL} = 12 mA
		$2.70 \leq V_{CC} \leq 3.60$		0.4		0.4		IOL - 12 IIIA
		$2.30 \leq V_{CC} < 2.70$		0.6		0.6		I _{OL} = 18 mA
		$2.70 \leq V_{CC} \leq 3.60$		0.4		0.4		
		$2.70 \leq V_{CC} \leq 3.60$		0.55		0.55	•	$I_{OL} = 24 \text{ mA}$
I _{IN}	Input Leakage Current	0.90 to 3.60		±0.1		±0.5	μA	$0 \leq V_I \leq 3.6V$
I _{OFF}	Power Off Leakage Current	0		0.5		0.5	μΑ	$0 \leq (V_I, V_O) \leq 3.6$
I _{CC}	Quiescent Supply Current	0.90 to 3.60		0.9		0.9	μA	$V_{I} = V_{CC} \text{ or } GND$
		0.90 to 3.60				±0.9	μΑ	$V_{CC} \le V_I \le 3.6V$

AC Electrical Characteristics

Symbol	Parameter	V _{CC}	$T_A = +25^{\circ}C$		$T_A = -40^{\circ}C$ to $+85^{\circ}C$		Units	Conditions	Figure Number	
Symbol	Faianeter	(V)	Min Typ Max		Min Max		Units			
t _{PHL}	Propagation Delay	0.90		15					$C_L = 15 \text{ pF}, \text{ R}_L = 1 \text{ M}\Omega$	
t _{PLH}		$1.10 \leq V_{CC} \leq 1.30$	3.0	6.0	15.0	1.0	18.6	•	C _L = 15 pF,	
		$1.40 \leq V_{CC} \leq 1.60$	1.0	3.2	8.7	1.0	9.7	20	$R_L = 2 \ k\Omega$	Figures
		$1.65 \leq V_{CC} \leq 1.95$	1.0	1.9	6.0	1.0	6.8	ns	$C_L = 30 \text{ pF}$	1, 2
		$2.30 \leq V_{CC} < 2.70$	0.8	1.2	3.6	0.7	4.7		$R_L = 500\Omega$	
		$2.70 \leq V_{CC} \leq 3.60$	0.7	1.0	3.3	0.6	4.0			
CIN	Input Capacitance	0		2.0				pF		
COUT	Output Capacitance	0		4.5				pF		
CPD	Power Dissipation	0.90 to 3.60		8				pF	$V_I = 0V \text{ or } V_{CC}$	
	Capacitance	0.90 10 3.60		0				рг	f = 10 MHz	



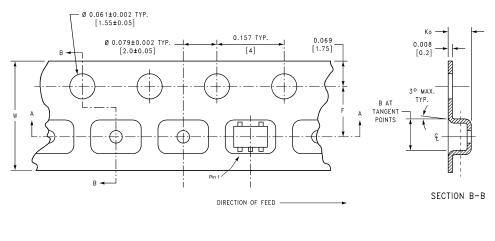
Symbol	V _{cc}							
Cymbol	$\textbf{3.3V} \pm \textbf{0.3V}$	$\textbf{2.5V} \pm \textbf{0.2V}$	$\textbf{1.8V} \pm \textbf{0.15V}$	$\textbf{1.5V} \pm \textbf{0.10V}$	$\textbf{1.2V} \pm \textbf{0.10V}$	0.9V		
V _{mi}	1.5V	V _{CC} /2	V _{CC} /2	V _{CC} /2	V _{CC} /2	V _{CC} /2		
V _{mo}	1.5V	V _{CC} /2	V _{CC} /2	V _{CC} /2	V _{CC} /2	V _{CC} /2		

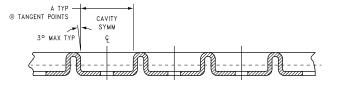


Tape and Reel Specification

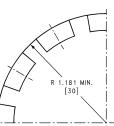
TAPE FORMAT for S	SC70			
Package	Таре	Number	Cavity	Cover Tape
Designator	Section	Cavities	Status	Status
	Leader (Start End)	125 (typ)	Empty	Sealed
P5X	Carrier	3000	Filled	Sealed
	Trailer (Hub End)	75 (typ)	Empty	Sealed

TAPE DIMENSIONS inches (millimeters)

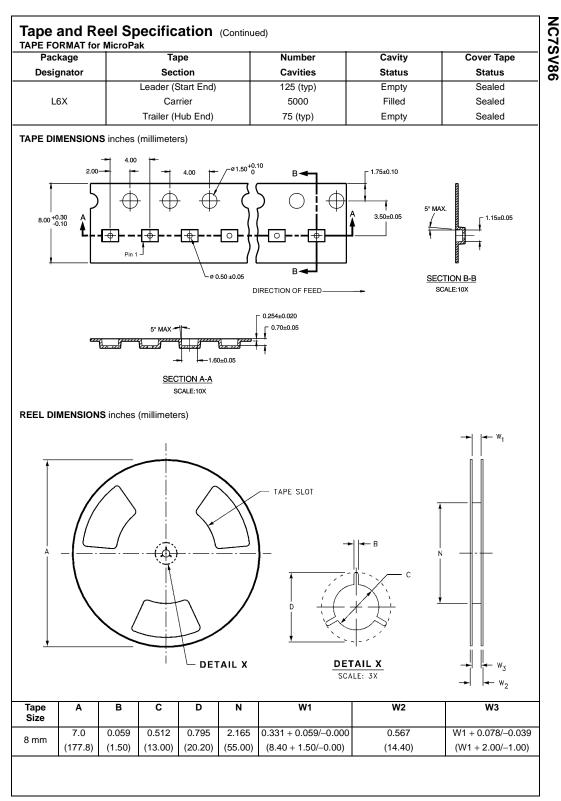


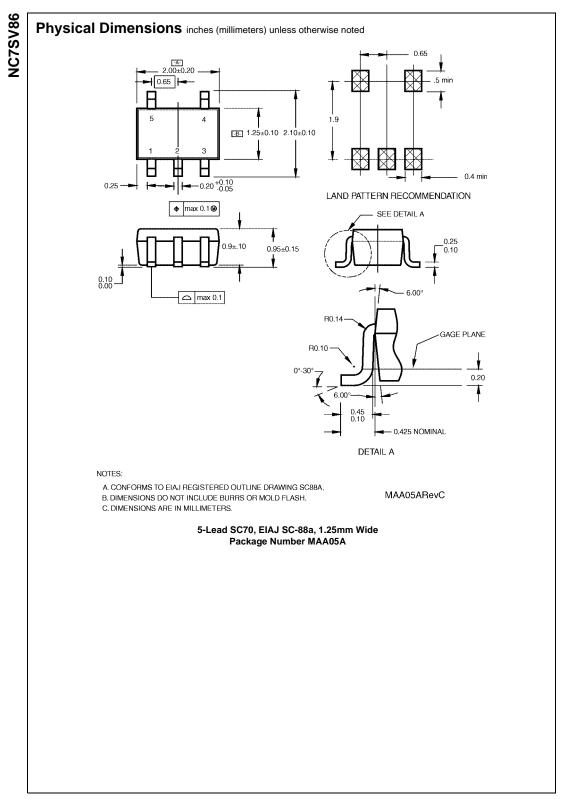


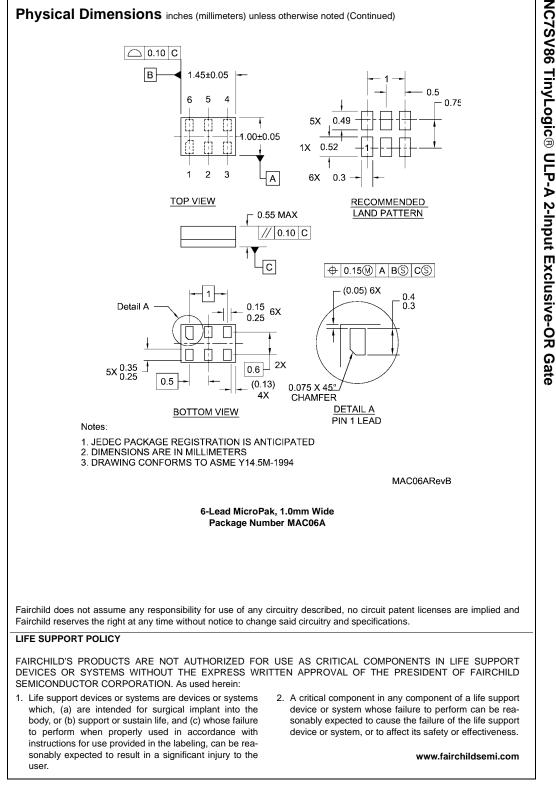
SECTION A-A



BEND RADIUS NOT TO SCALE







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